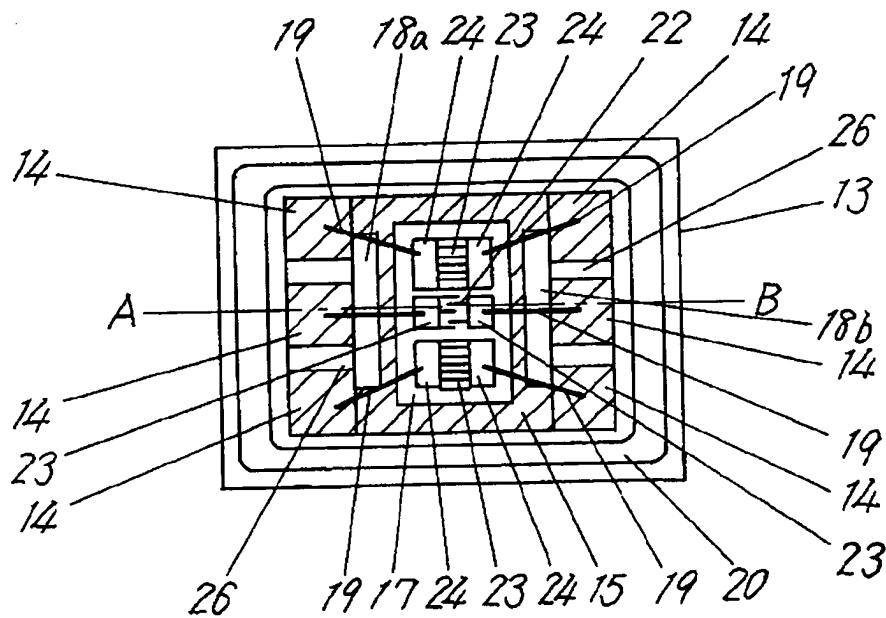




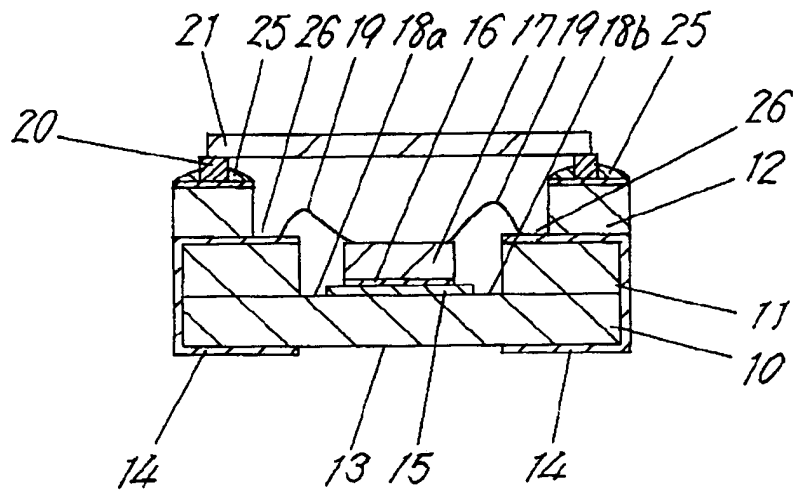
FIG. 1



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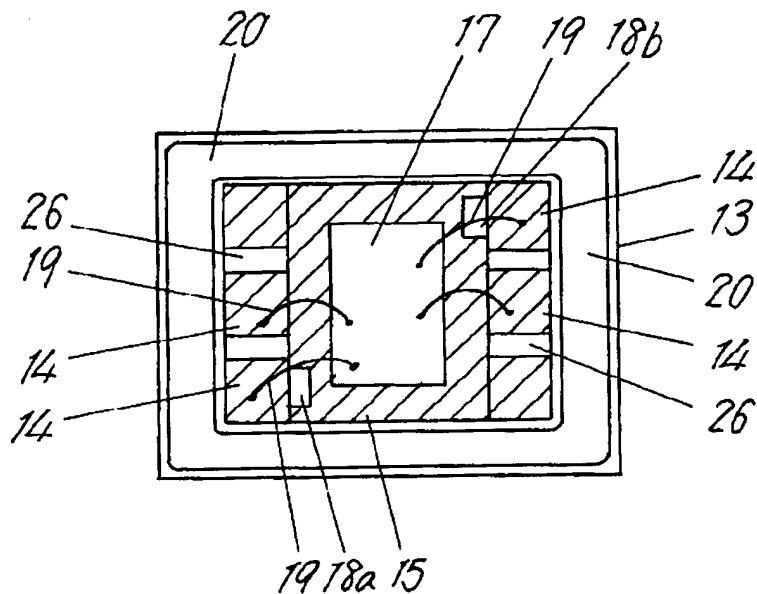
FIG. 2



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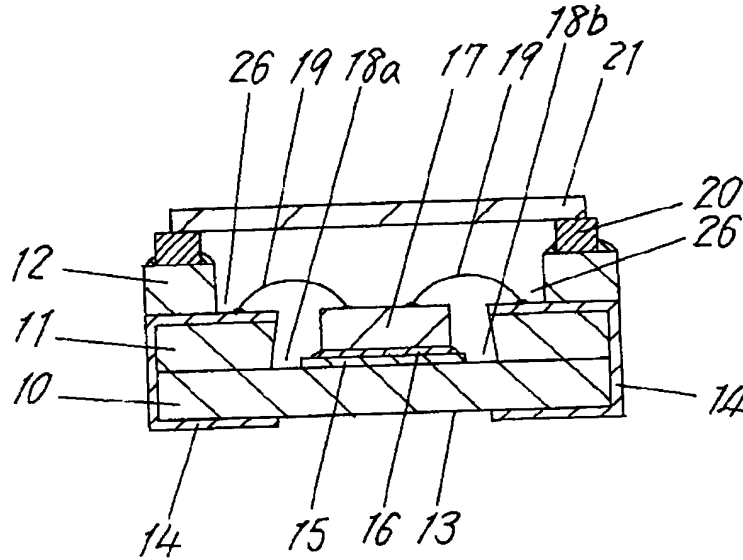
FIG. 3



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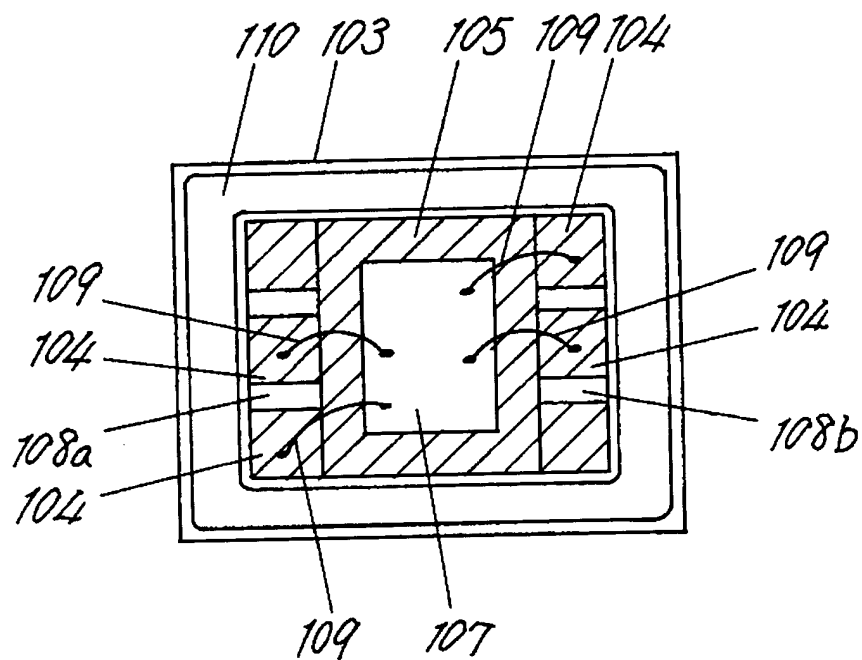
FIG. 4



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FIG. 5



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A cross-sectional view of a device, likely a semiconductor or microelectronic component. The device consists of a substrate (100) with a top layer (101) and a bottom layer (104). A central region (102) contains a structure (103) with a top layer (106) and a bottom layer (105). The device is surrounded by a protective layer (107) and a top layer (109). The device is mounted on a base (110) with a top layer (111) and a bottom layer (102).